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Readout Electronics for the CEPC Reference Detector

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The Electronics system of the CEPC reference detector has completed the design of the overall framework based on background simulation and the baseline readout electronics design of the sub-detectors. We will implement the overall design of the Electronics-TDAQ system based on a common backend electronics and a common trigger system design. The framework design of the front-end electronics system will be realized based on a common data-fiber interface and a common radiation-hard power module. Furthermore, based on the baseline schemes of each sub-detector system, a targeted front-end electronics design has been proposed, which will implement a front-end triggerless, full backend trigger architecture. This talk will introduce the design considerations of the CEPC Electronics-TDAQ system, the common electronics framework, and the recent main related R&D efforts.

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